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7590

11/07/2006

Mr. Jerry Churma
Semiconductor Components Industries, L.L.C.
Patent Administration Dept - MD/A/700
P.O. Box 62890
Phoenix, AZ 85082-2890

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LYDIA McNamara (Depositor's name)
Lydia McNamara (Signature)
11/14/06 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/813,501	03/31/2004	Alan R. Ball	ONS00555	4897

TITLE OF INVENTION: METHOD OF FORMING A SELF-GATED TRANSISTOR AND STRUCTURE THEREFOR

APPL. TYPE	SMALL ENTITY	ISSUE FEE DUE	PUBLICATION FEE DUE	PREV. PAID ISSUE FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1400	\$300	\$0	\$1700	02/07/2007

EXAMINER	ART UNIT	CLASS-SUBCLASS
TRA, ANH QUAN	2816	327-427000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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2. For printing on the patent front page, list

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1 Robert F. Hightower

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Semiconductor Components Industries, L.L.C.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Phoenix, Arizona, USA

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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- ☐ a. Applicant claims SMALL ENTITY status. See 37 CFR 1.27. ☐ b. Applicant is no longer claiming SMALL ENTITY status. See 37 CFR 1.27(b)(2).

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11/13/06

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38,502

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PTOL-85 (Rev. 07/06) Approved for use through 04/30/2007.

OMB 0651-0033

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